

PCB LAYOUT

Diagram showing a PCB layout with dimensions and tolerances:

- Top horizontal dimension: 3.63
- Top right horizontal dimension: 5.08 ± 0.03
- Left vertical dimension (top section): 10.00 ± 0.03
- Left vertical dimension (bottom section): 10.00
- Internal horizontal dimension: 2.54 ± 0.03
- Diagonal dimension: $\phi 1.50 \pm 0.10$ with a tolerance of 0.00

[illegible]

UL/CUL 技术 参数				
UL/CUL Technical Data				
等 级	Use Group	B	C	D
额定电压	Rated Voltage [V]	300	-	300
额定电流	Rated Current [A]	10	-	10
接线范围	Wire Range	-		
单/多芯线	Solid/Stranded [AWG]			
耐电压	Withstanding Voltage	AC1600V/1Min		
IEC 技术参数				
IEC Technical Data				
过电压类别	Overvoltage Category	III	III	II
污染等级	Contamination Class	3	2	2
额定电压	Rated Voltage [V]	-	320	630
额定电流	Rated Current [A]	-	15	15
额定冲击电压	Impulse withstand voltagea [KV]	-	4.0	4.0
导线截面积	Conductor cross section	-		
硬质/柔性导线	Solid/ flexible [mm ²]			
材 料 Material				
绝缘材料	Insulation Material	热塑性材料 Thermoplastic		
导体材料	Contactar Material	铜合金 Copper alloy		
导体表面镀层	Contactive Coating	镀锡 Tin Plated		
一般参数 Other Data				
间距 Pitch	[mm]	5.08		
极数 Poles	[P]	2-24		
剥线长度 Stripping Length	[mm]	-		
螺丝 Screw/ 扭矩 Torque(Max)	[N.m]	-		
工作温度 Operating temperatur	[°C]	-40~+105		
引针尺寸 Pin Dimensions	[mm]	4.2		
PCB板孔径 PCB Hole Diameter	[mm]	1.6		
安装类型 Mounting type		波峰焊 Wave soldering		

DES.	LQQ	DATE	21.03.13	单位 / UNITS	mm	尺寸 / SIZE	A4	比例 / SCALE	1:1	第三视角 / THIRD ANGLE PROJECTION		版本 / REV	A
CHK.	SY	DATE	21.03.13	 慈溪市科发电子有限公司 CIXI KEFA ELECTRONIC CO., LTD						名称 / TITLE	KF2EDGRTC-5.08		
APP.	CLF	DATE	21.03.13										